Rambus to Present Two Papers at 2011 IEEE Custom Integrated Circuits Conference

SUNNYVALE, Calif.--(BUSINESS WIRE)-- Rambus Inc. (NASDAQ:RMBS):

Who: Rambus Inc. (NASDAQ: RMBS)

Doubletree Hotel
San Jose, CA

When: September 18-21, 2011

Join Rambus, one of the world’s premier technology licensing companies, at the 2011 IEEE Custom Integrated Circuits Conference. At the conference, Rambus engineers will present the following papers:

Monday, September 19, 2011, 4:45 p.m. PT
"Power-Efficient I/O Design Considerations for High-Bandwidth Applications"
Doubletree Hotel, Fir Ballroom, Session 7-7

Rambus will present power-efficiency results from several generations of I/O interfaces, including foundational work in active-power reduction at a single rate for a symmetric system, as well as a subsequent application to a burst-mode asymmetric interface.

Monday, September 19, 2011, 5:00 p.m. PT
"An Output Structure for a Bi-Modal 6.4Gbps GDDR5 and 2.4-Gbps DDR3 Compatible Memory Interface"
Doubletree Hotel, Cascade/Sierra Ballroom, Poster Session M-7

In this session, Rambus will outline how a bi-modal x32 memory interface supports 6.4Gbps GDDR5 signaling, as well as 2.4Gbps DDR3 signaling, while achieving 11.6mW/Gbps and 27.7mW/Gbps energy efficiencies in GDDR5 and DDR3 modes, respectively.

About Rambus Inc.

Rambus is one of the world’s premier technology licensing companies. Founded in 1990, the Company specializes in the invention and design of architectures focused on enriching the end-user experience of electronic systems. Rambus’ patented innovations and breakthrough technologies help industry-leading companies bring superior products to market. Rambus licenses both its world-class patent portfolio, as well as its family of leadership and industry-standard solutions. Rambus has offices in California, North Carolina, Ohio, India, Germany, Japan, Korea, and Taiwan. Additional information is available at www.rambus.com.

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